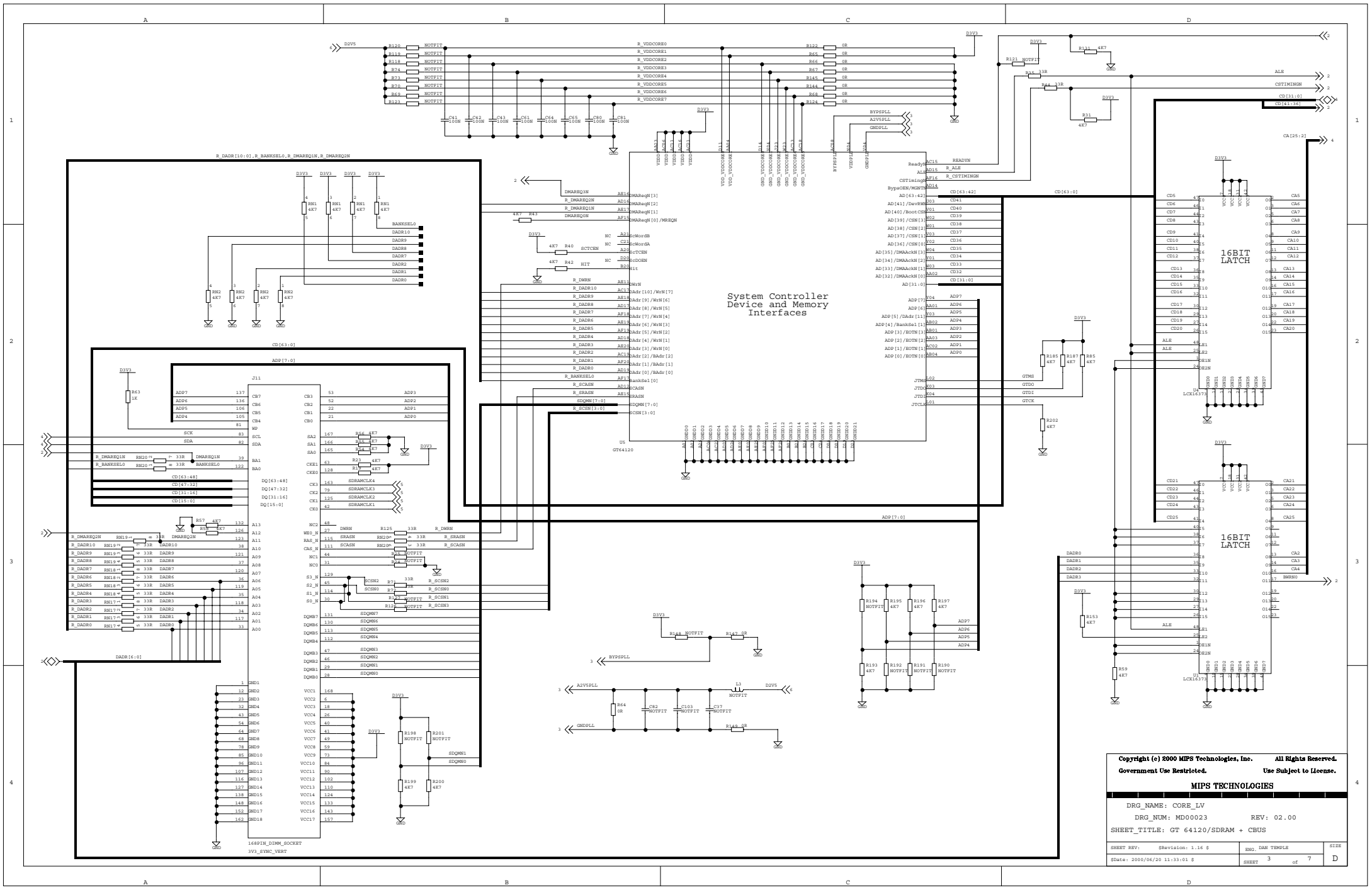


CORE_LV TM

Title Page	Sheet 1
GT64120 busses & EPLD	Sheet 2
GT64120 SDRAM & CBUS	Sheet 3
Atlas & LA connectors	Sheet 4
MIPS LV & clocking	Sheet 5
Power supplies	Sheet 6
CPU Debug & bond-out	Sheet 7

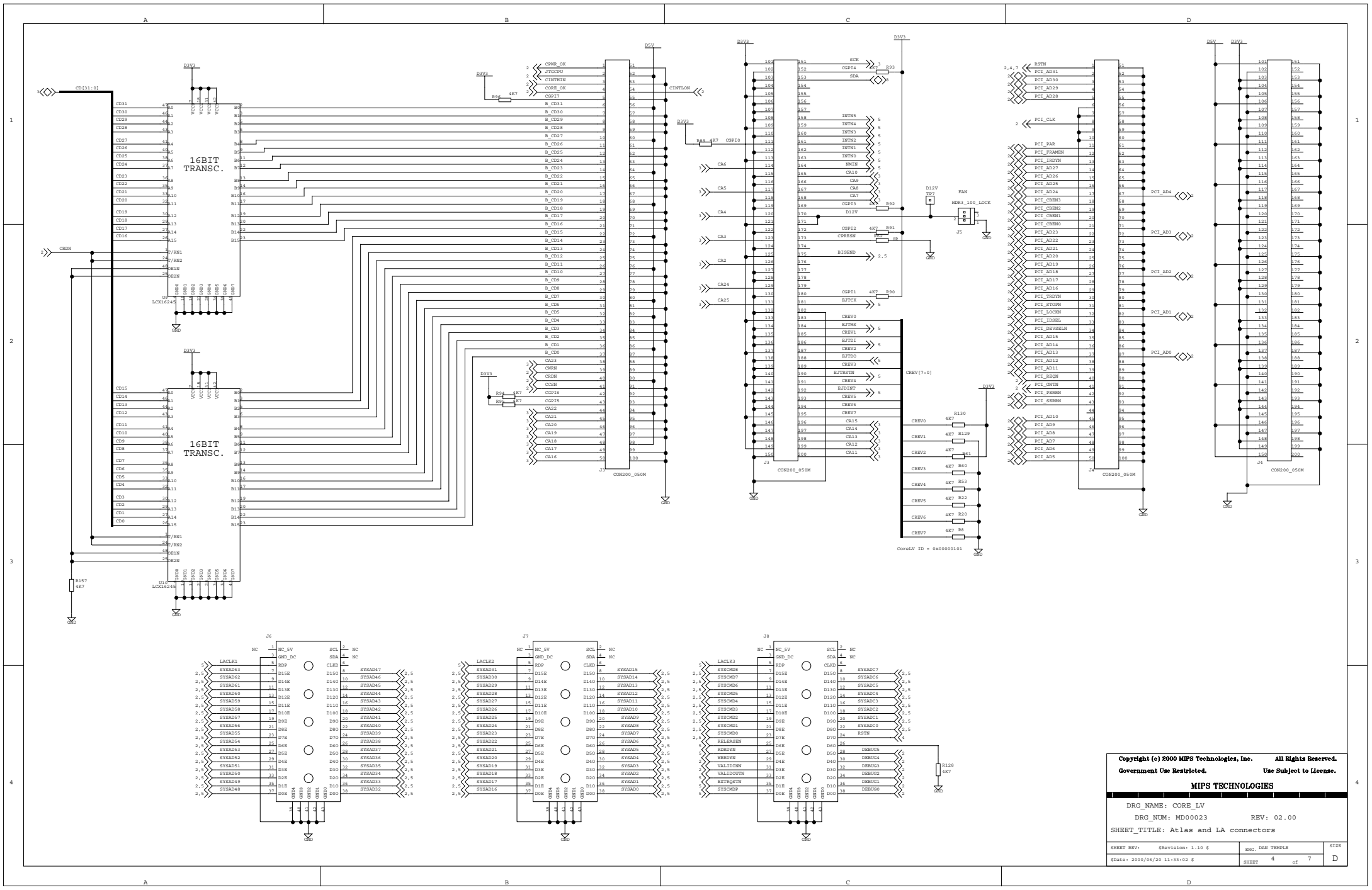
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MIPS TECHNOLOGIES			
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SHEET_TITLE: Contents			
SHEET REV: \$Revision: 1.7 \$	ENG: DAN THOMAS	SIZE	
SDate: 2000/05/04 14:59:44 \$	SHEET 1 of 7	D	



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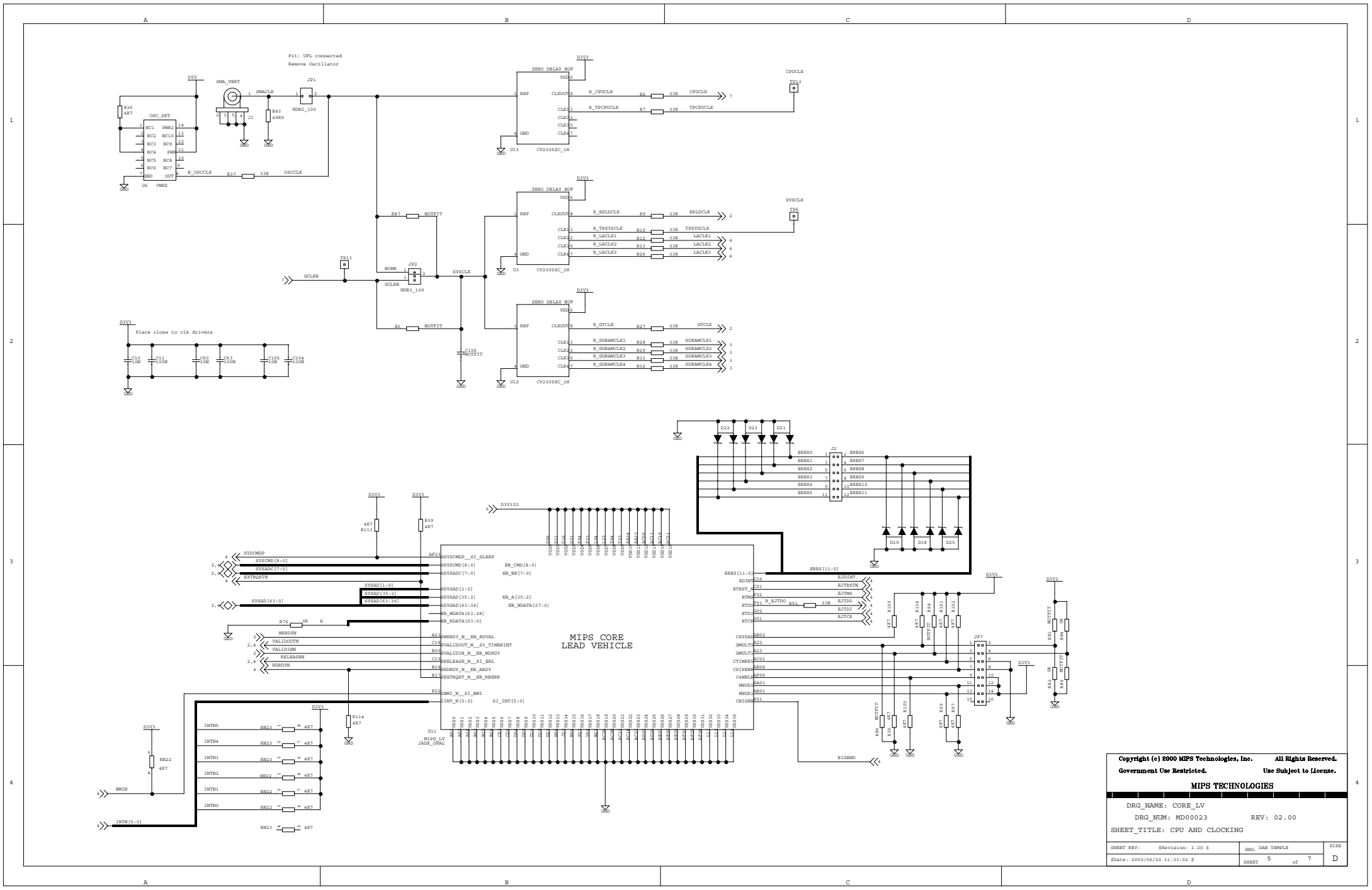
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SDate: 2000/06/20 11:33:01 \$	SHEET 3 of 7



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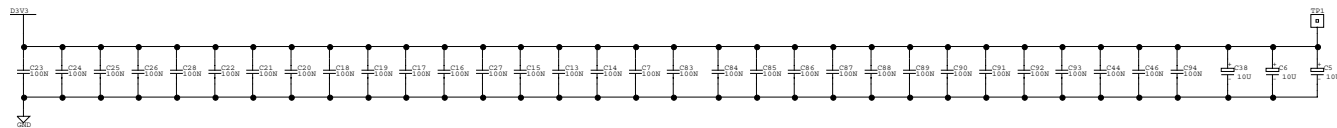
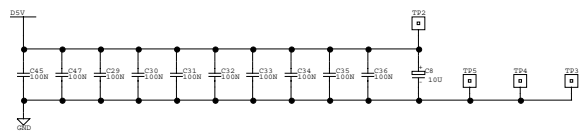
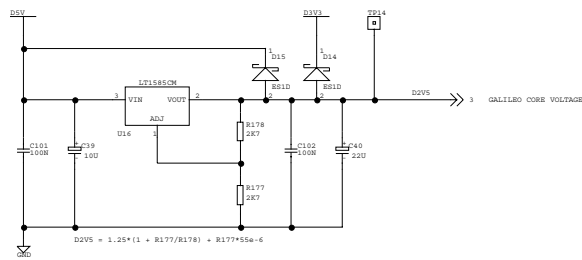
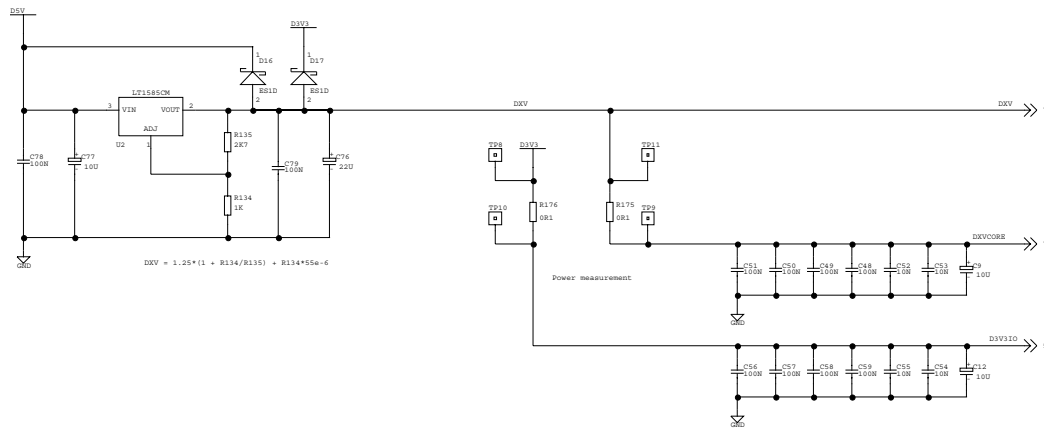
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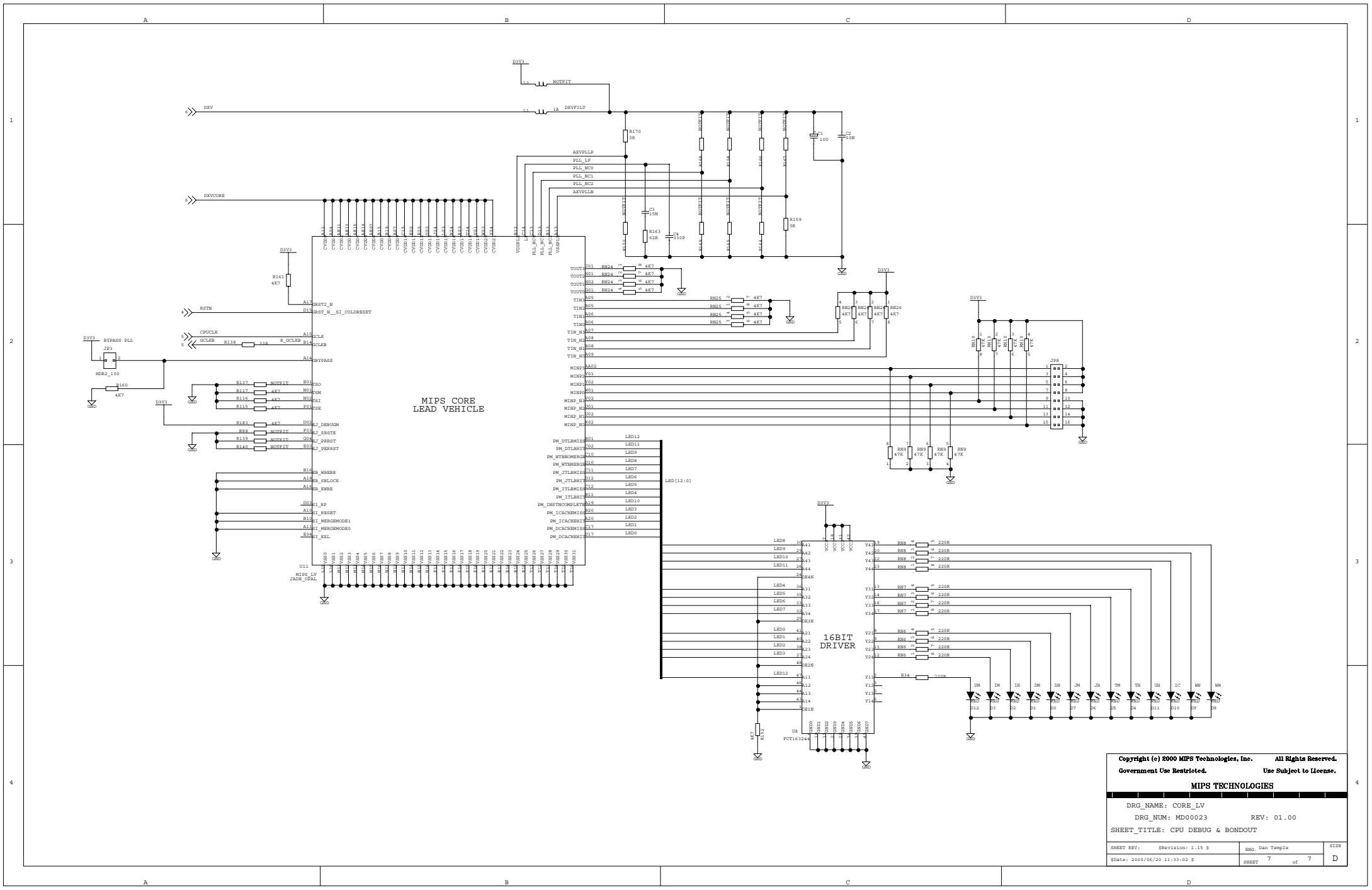
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DRG_NAME: CORE_LV
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SHEET_TITLE: CPU DEBUG & BONDOUT

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